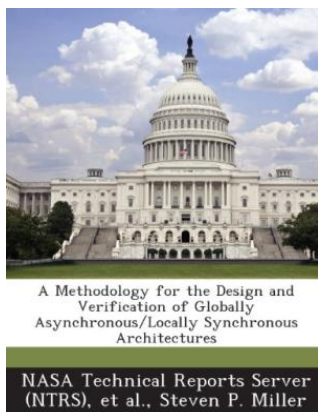


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A METHODOLOGY FOR THE DESIGN AND VERIFICATION OF GLOBALLY ASYNCHRONOUS/LOCALLY SYNCHRONOUS ARCHITECTURES



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